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**AMENDMENT UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE-EXAMINING  
GROUP 2814**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application of :  
Hirofumi HARADA :  
Serial No. 09/872,798 : Group Art Unit: 2814  
Filed: June 1, 2001 : Examiner: Thao X. Le  
For: VERTICAL MOS TRANSISTOR :  
AND A METHOD OF :  
MANUFACTURING THE SAME : Docket No. S004-4310

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COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, DC 20231

**AMENDMENT AFTER FINAL  
UNDER RULE 116**

S I R:

In response to the final Office Action dated  
November 28, 2002, applicant amends his application as  
follows:

**IN THE CLAIMS:**

Kindly cancel claims 19 and 20 without prejudice or  
admission.

Kindly amend claim 1 by rewriting it in amended form  
as follows: